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APPLICANTS Masahiro Takenaka, Nara-shi, JAPAN; ** <i>None MC</i> ** CONTINUING DATA *****				
 <i>OK MC</i> ** FOREIGN APPLICATIONS ***** JAPAN 2002-318894 10/31/2002				
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>Masahiro Takenaka MC</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS <i>20/19</i> CLAIMS 2
ADDRESS 23117 NIXON & VANDERHYE, PC 1100 N GLEBE ROAD 8TH FLOOR ARLINGTON , VA 22201-4714				
TITLE Method of semiconductor device fabrication				
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